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**Complete if Known**

Application Number

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First Named Inventor

Adrian J. Isles

Art Unit

2123

Examiner Name

Sharon, Ayal I.

Attorney Docket Number

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**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
Ayal		FUJITA, et al., "Speeding Up Look-Up-Table Driven Logic Simulation", VLSI 1999, pp 1-13.	
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Examiner  
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